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(51) INT CL<sup>5</sup>

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(54) High-frequency superconductive inductor for a power conversion system

(57) An inductor (10) uses high temperature superconductive material in order to obtain high Q for high frequency operation. The superconductive material is applied as thin film (16F) to substrate (12F or 12S). Conductive films (14F, 14S) may be applied to the outside of the inductor in order to shield against leakage of the magnetic field. The conductive films (14F, 14S) may be of superconductive material. The inductor may be formed with plural superconductive layers (16F, 16S). The superconductive films may be applied to opposite sides of the same substrate. The inductor may be connected to a capacitor to realize a resonant circuit for use in a power conversion system.

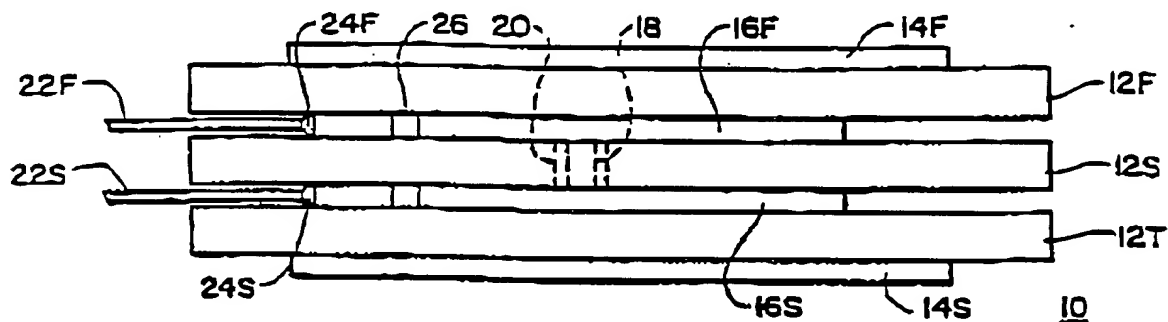


FIG. 1

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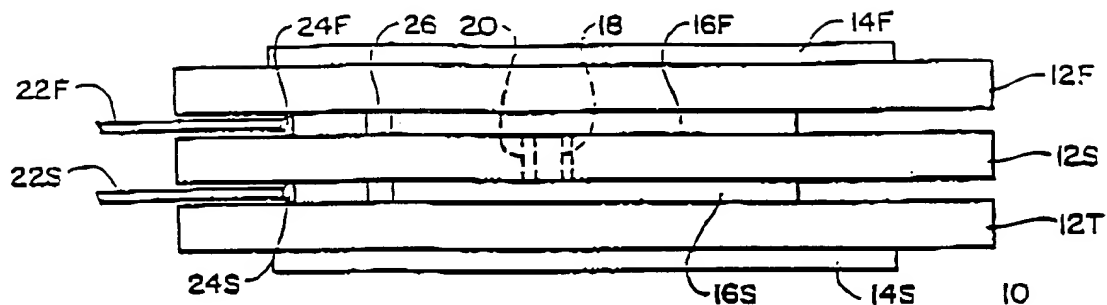


FIG. 1

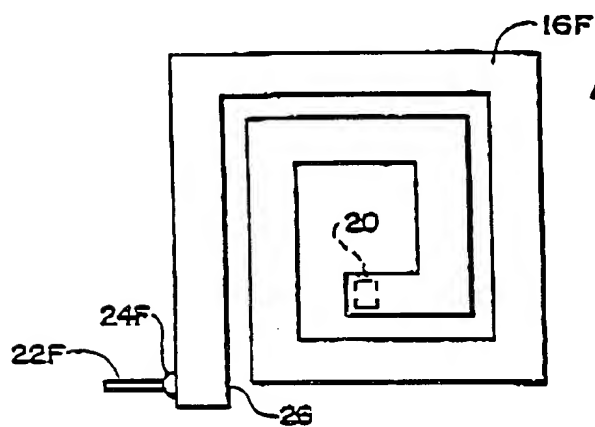


FIG. 2A

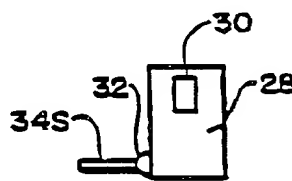


FIG. 2B

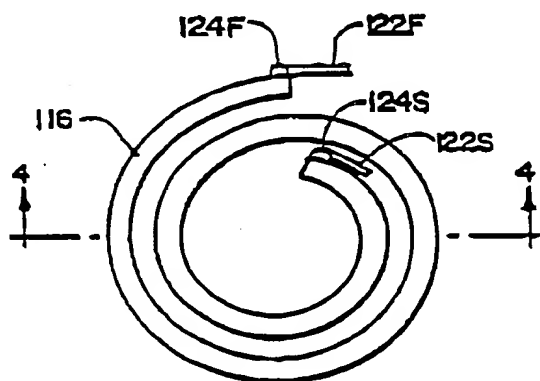


FIG. 3

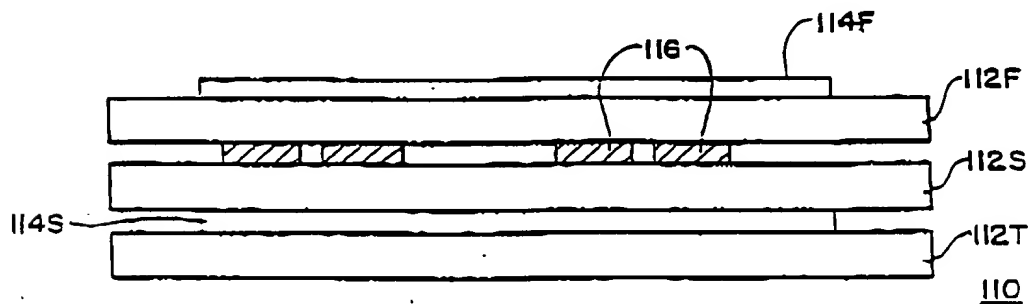
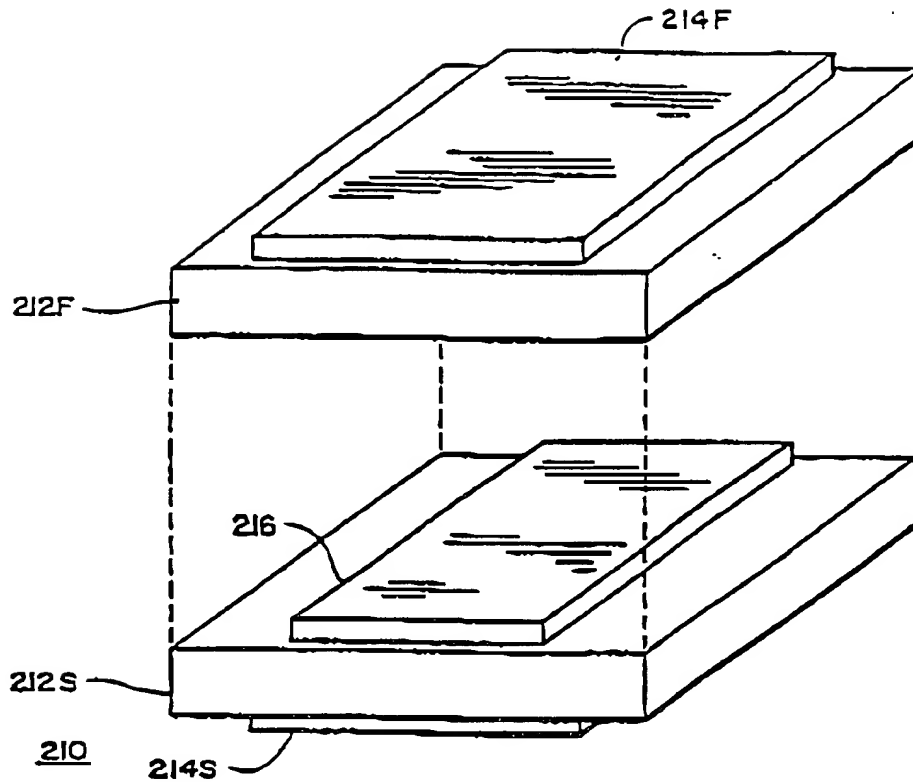
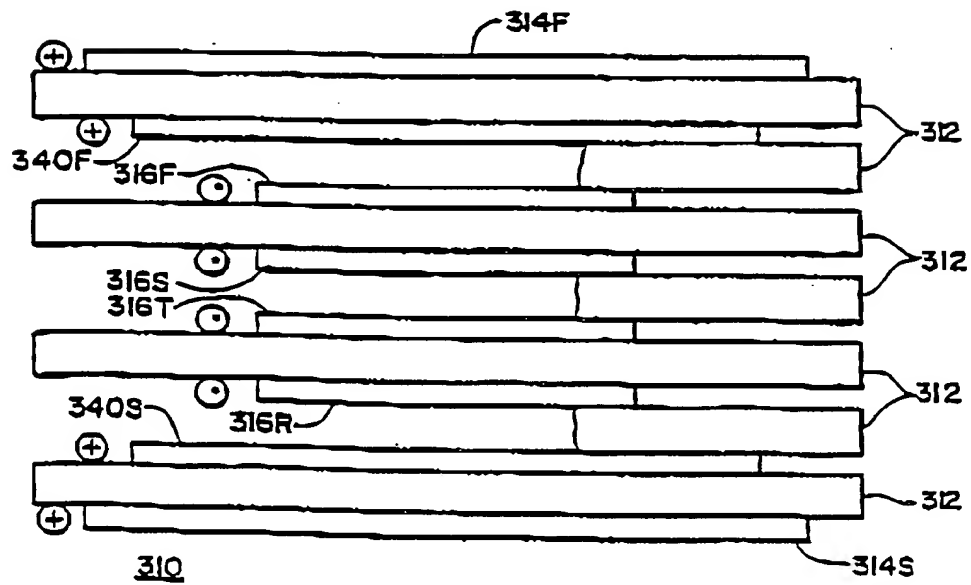


FIG. 4

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**FIG. 5****FIG. 6**

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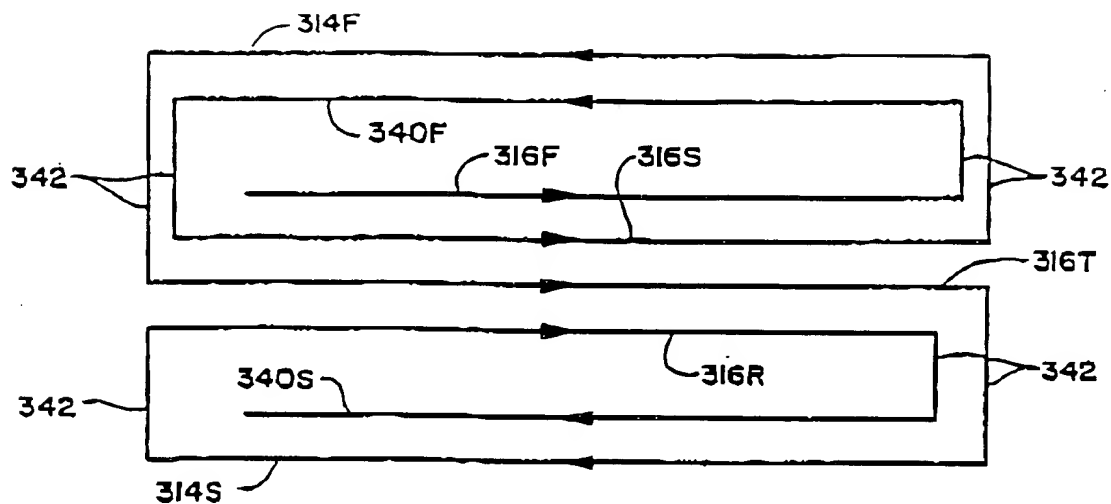


FIG. 7

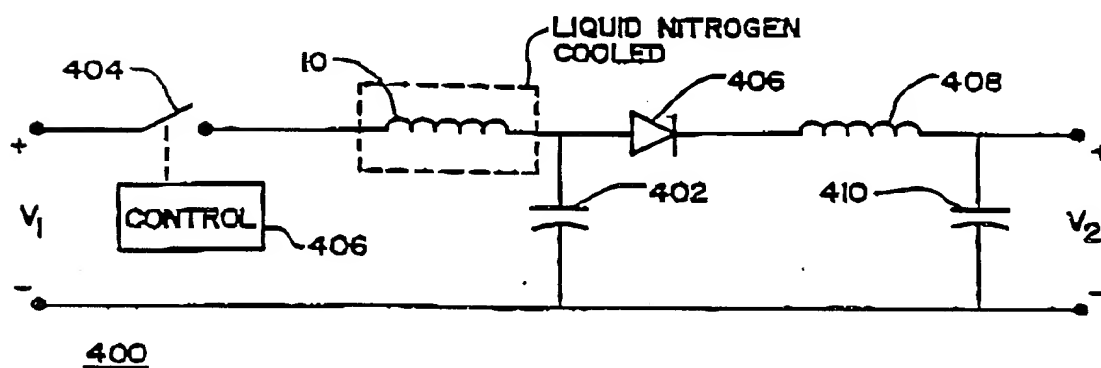


FIG. 8

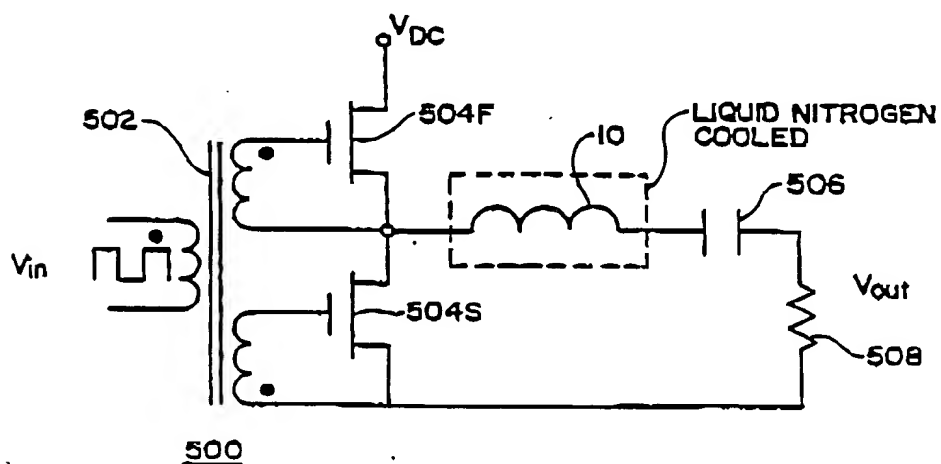


FIG. 9

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HIGH-FREQUENCY INDUCTORS USING  
SUPERCONDUCTORS AND POWER  
CONVERSION HIGH-FREQUENCY SYSTEMS  
USING THE INDUCTORS

Field of the Invention

The present invention relates to inductors made with superconductors to obtain high Q (quality factor) and systems using the inductor. More specifically, the present invention relates to an  
5 inductor useful for resonant circuits in various power conversion systems at frequencies of at least one MHz.

Background of the Invention

Various power conversion systems have been used over the years. Among such power conversion systems, converters are used to convert DC (direct  
10 current) to DC, whereas inverters are used for converting DC to AC (alternating current). Radio frequency power amplifiers perform high frequency power conversion by using a RF (radio frequency) input and a DC input to provide a RF output with a  
15 significantly higher power than the RF input.

In order to reduce the size of such power converters, power conversion frequencies have been pushed into the MHz range. The size reduction is primarily due to the smaller size of passive magnetic  
20 components as the frequency is increased. Currently, the power densities are of the order of 50-100 Watts/in<sup>3</sup>. Further improvement power densities (>200 Watts/in<sup>3</sup>) is required for the upcoming high performance electronic systems such as massively  
25 parallel supercomputers. For higher power densities, it is advisable to consider frequencies in 10-1000 MHz range. The circuit topologies for such high frequency

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power conversion are of the resonant type. The resonant inductor used in such designs is the most critical passive component. Specifically, high circulating currents greatly stress the inductor. Thus the inductor requires a very high quality factor, of the order of 1000.

It has been difficult, if not impossible, to fabricate an inductor having the necessary characteristics for such high frequency power conversion applications using normal metallic conductors such as copper. Specifically, the skin effect causes currents to flow essentially at the surface of conductors at higher frequencies. The relatively high surface resistance of normal conductive metals such as copper tends to reduce the Q of the inductor. As the frequency gets higher, the current is even more concentrated at the surface corresponding effectively to a reduction in the cross-sectional area through which the current may flow such that the resistance is increased further.

#### Objects of the Invention

Accordingly, it is a primary object of the present invention to provide an improved inductor.

A further object of the present invention is to provide a system using the improved inductor.

A more specific object of the present invention is to provide an inductor which uses superconductors to obtain high Q and high current-carrying capabilities.

Yet another object of the present invention is to provide an inductor which is well-shielded against emissions.

A further object of the present invention is to provide a system using the inductor as part of a

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resonant circuit with a capacitor to allow operation at relatively high operating frequencies of at least one MHz and, more specifically, in the range of 10 to 1,000 MHz.

#### Summary of the Invention

5           The above and other objects which will become apparent when the following detailed description is read in conjunction with the accompanying drawings are realized by an inductor having conductive first and second outer layers. A  
10   first inner layer is disposed between the first and second outer layers. The first inner layer has a magnetic field establishing first active conductor portion separated from both of the first and second  
15   outer layers. The first active conductor portion is made of superconductor material which has a critical temperature greater than 30°K. First and second external inductor leads (leads connected and  
20   positioned for connection of other circuit components) are on the inductor. There is a continuous conductor path from the first lead to the second lead and the first active conductor portion is part of the  
25   continuous conductor path. The path is a continuous conductor path in that electric current may flow from one end of the path to the other, while remaining on conductive material.

          The inductor is combined with a capacitor having two external capacitor leads, one of the capacitor leads connected to one of the inductor leads such that the inductor and capacitor form a resonant  
30   circuit having a resonant frequency of at least one MHz, and, more specifically, at least 10 MHz.

          Some embodiments of the present invention include a second inner layer having a magnetic field



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establishing second active conductor portion separated from both of the first and second outer layers. The second active conductor portion is made of a superconductor material which has a critical temperature greater than 30°K. The second active conductor portion is part of the continuous conductor path. A conductive first-to-second interlayer connection electrically connects the first and second active conductor portions. The first-to-second interlayer connection is part of the continuous conductor path. In a first of these embodiments, each of the first and second active conductor portions is a coil. In a second embodiment, each of the first and second outer layers includes a conductor portion which is made of superconductor material having a critical temperature greater than 30°K. These conductor portions of the outer layers are part of the continuous conductor path in this embodiment and the outer layers serve as shields limiting magnetic fields established by the inductor to between the first and second outer layers (In other words, the magnetic fields are "limited" to that zone in the sense that at least 95% of the energy from the magnetic fields which are established is stored between the first and second outer layers. More preferably, at least 99% of the stored energy is within that zone.)

In the first embodiment mentioned above and in a third embodiment, the outer layers serve as shields limiting magnetic fields established by the inductor to between the first and second outer layers. Unlike in the second embodiment, the shields are isolated from the continuous conductor path. The first active conductor in the third embodiment is a planar, straight strip.

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The inductor may further include first and second substrates with the first substrate disposed between the first outer layer and the first inner layer and the second substrate disposed between the second outer layer and the first inner layer. The first active conductor portion is a thin film disposed on one of the first and second substrates. Each of the first and second outer layers and first inner layer is planar and parallel to the others of the first and second outer layers and first inner layer. The critical temperature is more preferably higher than 70°K and the superconductor material has a critical current density of at least  $10^5$  A/cm<sup>2</sup>, more preferably at least  $10^6$  A/cm<sup>2</sup>.

The system of the present invention includes the inductor with one of its leads attached to a lead of a capacitor in order to form a resonant circuit having a resonant frequency of at least one MHz and, more specifically, at least 10 MHz. In a first embodiment of the system, the resonant circuit is part of a DC to DC converter. In a second embodiment of the system, the resonant circuit is part of an RF amplifier. In a third embodiment of the system, the resonant circuit is part of an inverter which converts DC into AC.

#### Brief Description of the Drawings

The above and other features of the present invention will be more readily understood when the following detailed description is considered in conjunction with the accompanying drawings wherein like characters represent like parts throughout the several views and in which:

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FIG. 1 shows a side view of a first embodiment inductor according to the present invention;

FIG. 2A shows a thin film pattern which may be used with the inductor of FIG. 1;

FIG. 2B shows a thin film pattern which may be used opposite to the pattern of FIG. 2A;

FIG. 3 shows a thin film pattern used with a second embodiment inductor according to the present invention;

FIG. 4 shows a side view of the second embodiment inductor;

FIG. 5 shows an exploded perspective view of a third embodiment inductor;

FIG. 6 shows a side view of a fourth embodiment inductor with portions of some substrates broken away;

FIG. 7 is a schematic illustrating current flow in the inductor of FIG. 6;

FIG. 8 is a schematic of a first embodiment system of the present invention; and

FIG. 9 is a schematic of a second embodiment system according to the present invention.

#### Detailed Description

FIG. 1 shows a first embodiment inductor 10 according to the present invention. The inductor 10 includes first, second, and third substrates 12F, 12S, and 12T respectively. Although other materials could be used, each of the substrates preferably is composed of  $\text{LaAlO}_3$ . Other materials having a relatively low dielectric constant and a structure compatible with a thin film superconductor as described below could be used for the substrates. Each of the substrates is preferably between 20 and 100 mils, this corresponding

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to a range of 0.0508 centimeters and to 0.254 centimeters. More preferably, the substrates will each have a thickness of about 30 mils, corresponding to 0.0762 centimeters.

5 First and second outer layers 14F and 14S respectively are disposed respectively upon substrates 12F and 12T. Each of the thin films 14F and 14S are made of a high temperature superconductor and serve as shielding to contain fields within the relatively  
10 small volume between the parallel, planar thin films 14F and 14S .

Disposed between substrates 12F and 12S is a first inner layer 16F. The first inner layer 16F is a thin film of superconductor applied in a pattern to  
15 one of the opposing surfaces of substrates 12F and 12S. In the view of FIG. 1, the thin film inner layer 16F would either be applied to the lower surface of substrate 12F or to the upper surface of substrate 12S. In similar fashion, a second inner layer 16S of  
20 thin film superconductor is disposed between substrates 12S and 12T and deposited upon one of the opposing surfaces of those two substrates. Preferably, the thin film inner layers 16F and 16S are both deposited to the substrate 12S. Each of the  
25 layers 16F and 16S may be considered to be an active layer having a magnetic field establishing active conductor portion made of superconductive material. As used herein, such a magnetic field establishing active conductor portion is a conductive portion of an  
30 inductor to which current is applied in order to generate the magnetic field of the inductor. Thus, since outer layers 14F and 14S do not have current applied to them to establish magnetic fields, the

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outer layers 14F and 14S are not magnetic field establishing active conductor portions as used herein.

As shown in FIG. 1, a via hole 18 extends through substrate 12S and has a conductive first-to-second interlayer via connection 20 connecting the first inner layer 16F to the second inner layer 16S. Basically, each of the layers 16F and 16S are turns of the inductor 10 and the turns are connected in series by the connection 20. The connection 20 would preferably be a thin film of the same superconductor used for layers 16F and 16S, although other conductors, including conductors which are not superconductive, might be used as well.

External inductor leads 22F and 22S are respectively connected to layers 16F and 16S by way of respective gold portions 24F and 24S. The gold portions 24F and 24S, which may be deposited to the side of the thin film superconductor 16F and 16S as shown or deposited completely or partly on top of the thin films, are used so that the leads 22F and 22S may be welded to them for electrical connection to the thin films. As shown, there is a continuous conductor path from lead 22F to lead 22S, which continuous conductor path includes gold portions 24F and 24S, thin film first and second inner layers 16F and 16S, and connection 20.

With reference now to FIG. 2A, the pattern of thin film inner layer 16F is shown as a rectangular spiral pattern having two turns separated by a space 26. In actual practice, one may use three, four, five, or possibly more turns. The pattern of thin film inner layer 16S may be identical to that shown for 16F in FIG. 2A and the layers 16F and 16S would have their patterns in registry, meaning that, if the pattern

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corresponding to 16S was added to FIG. 2A, it would simply consist of phantom lines directly below the corresponding lines in the pattern of layer 16F as illustrated. Both layers 16F and 16S would be planar and parallel.

As an alternative to having both inner layers constructed with the same pattern, one of the inner layers could be constructed like the pattern shown for layer 16F of FIG. 2A, whereas the other inner layer could be constructed as a strip 28 disposed on the opposite side of a substrate (not shown) from the inner layer 16F and connected to the inner layer 16F by a connection 30 (constructed like connection 20 of FIGS. 1 and 2A). The strip 28, which may be made of superconductor or a conductor which is not superconductive, is connected by gold portions 32 to an external lead 34S. Since the strip 28 is used in combination with components 30, 32, and 34S to carry away current from a rectangular spiral pattern such as shown for inner layer 16F of FIG. 2A, the strip 28 need not be made of superconductive material. The via connection 30 of FIG. 2B would be connected to a pattern such as inner layer 16F at the location shown for the via connection 20 in FIG. 2A.

FIG. 3 shows a superconductive thin film pattern inner layer 116 which may be used in a second embodiment inductor 110 shown more completely in FIG. 4. In the second embodiment inductor of FIGS. 3 and 4, each component has numbers in the "100" series with the same last two digits as the corresponding component in the embodiment of FIGS. 1 and 2A. Thus, substrates 112F, 112S, and 112T are constructed like the substrates 12F, 12S, and 12T of FIG. 1. First and second outer layers 114F and 114S are thin film

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superconductors applied respectively to substrates 112F and 112T. Layers 114F and 114S are shields like those of FIG. 1. As with the embodiment of FIG. 1, the shield layers 114F and 114S extend beyond the outer - 9 edges of the inner layer 116 so as to insure complete shielding of the magnetic fields established by the inner layer 116. The inner layer 116 is a thin film superconductor disposed upon the middle substrate 1125. For ease of illustration, only the inner layer 116 is shown in cross section in FIG. 4, corresponding to the cross section taken along lines 4-4 of FIG. 3. External leads 112F and 112S (FIG. 3 only) are used for making connections with the inductor 110. Gold portions 124F and 124S may be used as interfaces between the layer 116 and external leads 122F and 122S. The external lead 1225 may include an insulated portion (not separately shown) so that it may proceed across one of the turns in the spiral pattern of inner layer 116. As an alternative, the inner end of the spiral inner layer 116 could have an external lead connected to it by way of an arrangement like that of FIG. 2B with a strip similar to 28 of FIG. 2B on the side of substrate 1125 opposite two layer 116. In that case, the outer shielding layer 1145 would have to be disposed on the outside layer of substrate 112T instead of on the inner surface of substrate 112T (i.e., the position illustrated in FIG. 4). As an alternative to having the external lead 1225 proceed across part of the spiral of inner layer 116, the external lead 1225 could simply wind in between the two turns of the spiral inner layer 116.

Although FIG. 3 shows the spiral 116 having only two turns, it should be appreciated that in practice three, four, five, or possibly more turns

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would be used. It will also be appreciated that two of the circular spirals such as inner layer 116 could be mounted on opposite sides of a substrate (not illustrated) and connected in series by a via connection in similar fashion to that discussed above with respect to the rectangular spirals of inner layers 16F and 165 in FIG. 1.

FIG. 5 shows a third embodiment inductor 210 accordingly to the present invention. In the FIG. 5 embodiment, components are in the "200" series with the same last two digits as the corresponding component in the FIG. 1 embodiment. The inductor 210 includes substrates 212F and 2125 respectively having shielding outer layers 214F and 2145. The substrates and outer layers are constructed as discussed with respect to the corresponding components of FIG. 1. The inductor 210 is different from the FIG. 1 embodiment in that the inner layer 216 is simply a thin film strip of superconductor to which external leads (not shown) would be connected to opposite ends using the techniques described above. The current through the inner layer 216 establishes a magnetic field which would be confined within the outer layers 214F and 2145. The arrangement of inductor 210 is somewhat similar to a microstrip-line configuration.

FIG. 6 shows a fourth embodiment inductor 310 and has components in the "300" series with the same last two digits as the corresponding components in the FIG. 1 embodiment. The inductor 310 has a relatively large number of substrates which are simply labeled 312. All of the substrates may be identical in size and rectangular shape. However, for ease of illustration every other substrate has been shown with its left portion broken away. Shielding thin film



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superconductor outer layers 314F and 314S are used as conduits for the applied current as well as shields in the arrangement of FIG. 6. Intermediate thin film superconductor layers 340F and 340S are deposited on the outer substrates 312, whereas first, second, third, and fourth inner superconductor thin film layers 316F, 316S, 316T, and 316R are disposed on various of the substrates 312 as illustrated. With reference now also to FIG. 7, the inductor 310 serves as a flat version of the co-axial transmission line. Note also that the left side of FIG. 6 shows plus symbols adjacent those superconductor layers in which current is flowing out of the plane of view of FIGS. 6 and dot symbols for those thin film superconductor layers in which current is flowing into the plane of view of FIG. 6. FIG. 7, which shows a view from a right side 90° to the view of FIG. 6, shows how various end connectors 342 are used to link the various thin film superconductor layers in a pattern such that current flows from 316F to 340F to 316S to 314F to 316T to 314S to 316R to 340S. The arrangement of FIG. 6 and 7 is a four turn inductor. For ease of illustration, the external leads are not shown in FIGS. 6 and 7, but external leads would be used and connected to layers 316F and 340S using the same techniques discussed above.

In the arrangement of FIG. 6, the various thin film superconductor layers are disposed upon opposite sides of every other one of the substrates 312. In other words, each of the substrates 312 which has one thin film superconductor layer on it has another such layer on the opposite side. Further, every other substrate 312, corresponding to those substrates 312 which are only partially illustrated,

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have no thin film superconductor layers on them. If desired, these last mentioned substrates 312 could be replaced with some alternate insulation such as a Kapton layer deposited on top of the thin film which is to be insulated.

Thin film superconductors used for the various inductor designs discussed would preferably be made of  $\text{YBa}_2\text{Cu}_3\text{O}_7$  (YBCO). These have been shown to have a surface resistance at 10 GHz and 77° K which is orders of magnitude below that of copper. In general, other high temperature superconductor systems such as the thallium and bismuth systems are applicable to various embodiments of this invention. The relative advantage of superconductors over regular conductors increases at lower frequencies.

In fabricating the inductor, various methods of depositing the thin film of superconductor may be used. A preferred technique is evaporation, which is suitable to two-sided deposition so as to minimize the number of substrates needed. The YBCO thin films would be coevaporated and post-annealed using known techniques. Since deposition is at ambient temperature onto rotating plates, large area and double sided deposition is simplified.

The preferred range of thicknesses for the various superconductive thin films is between 4,000 and 8,000 Angstroms. A preferred thickness would be about 6,000 Angstroms. As used herein, a thin film has a thickness of about 6,000 Angstroms.

The various thin film superconductors used with the present invention should be high temperature superconductors having a critical temperature of greater than 30° K and, more preferably, having a critical temperature higher than 70° K.

The Q of the inductor made using the present techniques should be higher than 400 and, more

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preferably, between 5,000 and 10,000. Although various inductance values may be obtained, an inductance of 0.2 micro-Henrys may be obtained.

Since the inductor of the present invention  
5 is to be employed in a high power application, the superconductor must be able to handle high currents without reverting to the normal state. The thin film superconductor should have a critical current density of at least  $10^5 \text{A/cm}^2$ , and more preferably a current  
10 density higher than  $10^6 \text{A/cm}^2$ . The inductors constructed using the present techniques should be able to handle currents of 10 to 12 amps, although lower and higher maximum currents might occur in some inductors. The power handling capabilities of  
15 inductors according to the present invention should be at least 10 watts and, more specifically, at least 100 watts. Even more specifically, the power handling capabilities should be 10-1000 watts.

Turning now to FIG. 8, a system 400  
20 according to the present invention uses the inductor 10 in combination with capacitor 402 in order to realize a parallel LC resonant circuit. Specifically, one of the external leads (not separately shown in FIG. 8) would be connected to a corresponding external  
25 lead, either directly or by way of intermediate wiring, of the capacitor 402. As illustrated, the inductor 10 would be cooled by liquid nitrogen in order to achieve the superconductive effect. The resonant circuit of inductor 10 and capacitor 402  
30 would be used for DC to DC conversion in the system 400. In particular, an input switch 404 is controlled by control 406 to open and close at a frequency around the resonant frequency of the resonant circuit. Known techniques may be used for control of the switch 404,

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which is preferably a high power FET. At the output of the resonant circuit, a diode 406 is used for rectification, whereas an output inductor 408 and output capacitor 410 are used for filtering. The system 400 uses the inductor 10 (or any of the other inductors according to the present invention as discussed above) for converting an input DC voltage V1 into an output DC voltage V2.

Since liquid nitrogen or other low temperature cooling equipment is required for the inductor 10, the inductor 10 (and the other inductors made according to the present invention) is especially well suited to operate in a larger system where low temperature equipment is already in place. Among such systems are magnetic resonance imaging systems (where low temperature is needed for the superconducting magnet coils), infrared detector systems which use cooled materials such as semiconductors, cryogenic electronics, and space applications. The inductors according to the present invention may also be used in some high end computers where a form of liquid cooling is already provided. Under some circumstances, the inductors according to the present invention may be useful in systems which would not otherwise have liquid cooling. In that case, liquid cooling would have to be provided solely for use by the inductor.

In the arrangement of FIG. 8, the resonant inductor 10 is used to shape the current and voltage wave forms so as to reduce by a large factor the high frequency voltage and/or current stresses on the switch 404. In the absence of the resonant inductor, the high frequency stresses on the primary switch 404 could become formidable. The resonant inductor 10 makes the high frequency power converter 400 possible.

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Other high frequency converters (not shown) could use more than one resonant-type inductor. For example, one of the resonant inductors could be used as part of a tuned rectifier.

5       With reference now to FIG. 9, an inductor  
10       10 (or other inductor according to the present  
invention) may be used in a system 500. The system  
500 is a known design for a RF power amplifier and  
input RF voltage  $V_n$  is supplied to a transformer 502  
10       having first and second semiconductor switches 504F  
and 504S connected to different secondaries of the  
transformer 502. A series LC circuit including  
inductor 10 and a capacitor 506 are connected between  
the transformer 502 and the load 508 to which an  
15       amplified RF signal is applied as voltage  $V_{out}$ . A DC  
voltage  $V_{DC}$  is applied to FET switch 504F. The system  
500 can, in addition to its use as an RF power  
amplifier, be used as a high frequency inverter to  
convert the voltage  $V_{DC}$  into an AC signal  $V_{out}$

20       Although various specific constructions  
have been described herein, it is to be understood  
that these are for illustrative purposes only.  
Various modifications and adaptations will be apparent  
to those of skill in the art. Accordingly, the scope  
25       of the present invention should be determined by  
reference to the claims appended hereto.

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CLAIMS:

1. An inductor comprising: a conductive first outer layer; a conductive second outer layer; a first inner layer in between said first and second outer layers, said first inner layer having a magnetic field establishing first active conductor portion separated from both of said first and second outer layers, said first active conductor portion being made of superconductor material which has a critical temperature greater than 30°K; first and second external inductor leads; and wherein there is a continuous conductor path from said first lead to said second lead, said first active conductor portion being a part of said continuous conductor path.
2. The inductor of Claim 1 combined with a capacitor having two external capacitor leads, one of said capacitor leads connected to one of said first and second external inductor leads, wherein said inductor and said capacitor form a resonant circuit having a resonant frequency of at least 1 MHz.
3. The inductor of Claim 1 further comprising a second inner layer between said first and second outer layers, said second inner layer having a magnetic field establishing second active conductor portion separated from both of said first and second outer layers, said second active conductor portion being made of superconductor material which has a critical temperature greater than 30°K and being a part of said continuous conductor path and a conductive first-to-second interlayer connection electrically connecting said first and second active conductor portions, said first-to-second interlayer connection being a part of said continuous conductor path.

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4. The inductor of Claim 3 wherein each of said first and second active conductor portions is a coil.

5. The inductor of Claim 3 wherein each of said first and second outer layers is part of said continuous conductor path, and wherein said first and second outer layers are shields limiting magnetic fields established by the inductor to between said first and second outer layers.

10 6. The inductor of Claim 3 wherein said first and second outer layers are shields limiting magnetic fields established by the inductor to between said first and second outer layers, said shields isolated from said continuous conductor path.

15 7. The inductor of Claim 6 wherein each of said shields is made of superconductor material which becomes superconductive at a critical temperature greater than 30°K.

20 8. The inductor of Claim 1 wherein said first and second outer layers are shields limiting magnetic fields established by the inductor to between said first and second outer layers, said shields isolated from said continuous conductor path and wherein said first active conductor is a planar, straight strip.

25 9. The inductor of Claim 1 further comprising first and second substrates, said first substrate disposed between said first outer layer and said first inner layer, and said second substrate disposed between said second outer layer and said first inner layer, and wherein said first active conductor portion is a thin film disposed on one of said first and second substrates.

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10. The inductor of Claim 9 wherein each of said first and second outer layers and first inner layer is planar and parallel to the others of said first and second outer layers and first inner layer.

5 11. The inductor of Claim 1 wherein said critical temperature is higher than 70°K and said superconductor material has a critical current density of at least  $10^5$  A/cm<sup>2</sup>.

10 12. An inductor comprising: first and second active layers separated from each other and having corresponding magnetic field establishing first and second active conductor portions made of superconductive material having a critical temperature greater than 30° K; a conductive first-to-second  
15 interlayer connection electrically connecting said first and second active conductor portions, said first-to-second interlayer connection, and said first and second active layers are all part of a continuous conductor path; and first and second external inductor  
20 leads linked respectively to said first and second active conductor portions for application and removal of power from the inductor.

25 13. The inductor of Claim 12 wherein said first-to-second interlayer connection is made of superconductor material which becomes superconductive at a critical temperature greater than 30° K.

14. The inductor of Claim 13 wherein said first and second external leads are part of said continuous conductor path.

30 15. The inductor of Claim 11 further comprising first, second, and third substrates, said first outer layer and second outer layer each includes a thin film made of superconductor material which has a critical temperature greater than 30° K and



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respectively disposed on outer surfaces of said first and third substrates, said first active conductor portion being a thin film disposed on one of said first and second substrates and disposed between said first and second substrates, and said second active conductor portion being a thin film disposed on one of said first and third substrates and disposed between said first and third substrates.

16. A system comprising: an inductor having first and second external inductor leads with a continuous conductor path between them, said continuous conductor path including a first inner layer having a magnetic field establishing first active conductor portion, said first active conductor portion being made of a superconductive material which has a critical temperature greater than 30° K; and a capacitor having two external capacitor leads, one of said capacitor leads connected to one of said first and second external inductor leads, wherein said inductor and said capacitor form a resonant circuit having a resonant frequency of at least 1 MHz.

17. The system of Claim 16 wherein said inductor further includes first and second outer layers limiting magnetic fields established by the inductor to between said first and second outer layers, said first and second outer layers being made of superconductor material which has a critical temperature greater than 30°K.

18. The system of Claim 17 wherein said resonant frequency is at least 10 MHz.

19. The system of Claim 18 wherein said inductor further includes first and second substrates, said first substrate disposed between said first outer layer and said first inner layer, and said second

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substrate disposed between said second outer layer and said first inner layer, and wherein said first active conductor portion is a thin film disposed on one of said first and second substrates.

5           20. The system of Claim 19 wherein said resonant circuit is part of a system selected from the group comprising:

          a DC to DC converter;  
          an RF amplifier; and  
10          an inverter to convert DC into AC.

21. An inductor substantially as hereinbefore described with reference to FIGURES 1 to 6.

22. A system comprising an inductor and a capacitor substantially as hereinbefore described with reference to FIGURE 7 or 8.

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Patents Act 1977  
Examiner's report to the Comptroller under Section 17  
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### Relevant Technical Fields

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(ii) Int Cl (Ed.5) H01F

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J A WATT

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### Databases (see below)

(i) UK Patent Office collections of GB, EP, WO and US patent specifications.

Documents considered relevant following a search in respect of Claims :-  
1-11, 15 and 21

(ii) Online database: WPI

### Categories of documents

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| <p>X: Document indicating lack of novelty or of inventive step.</p> <p>Y: Document indicating lack of inventive step if combined with one or more other documents of the same category.</p> <p>A: Document indicating technological background and/or state of the art.</p> | <p>P: Document published on or after the declared priority date but before the filing date of the present application.</p> <p>E: Patent document published on or after, but with priority date earlier than, the filing date of the present application.</p> <p>&amp;: Member of the same patent family; corresponding document.</p> |
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Category	Identity of document and relevant passages		Relevant to claim(s)
Y	GB 1004178 A	(I B M) see lines 12-20 page 1	1-5 at least
X	EP 0455527 A1	(SUMITOMO) Figures 1-3 and lines 37-54 page 1	1
A	EP 0306287 A2	(S E L) see Figures 1 and 2	1-11, 15
Y	EP 0300556 A1	(PHILIPS) Figures 1-5. Note sheilding layer 21, 121	1-5 at least
Y	EP 0299116 A1	(PHILIPS) Figure 2 multi-layer superconductive coil	1-5 at least
Y	US 4968961 A	(HITACHI) Figures 1-3, Note stainless steel case 4 and shield (9, 13)	1-5 at least
Y	US 4032959 A	(W A R F) see lines 1-11 column 2	1-5 at least

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